

Linear Support Vector Machines via Dual Cached Loops

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ABSTRACT

Modern computer hardware offers an elaborate hierarchy of storage subsystems with different speeds, capacities, and costs associated with them. Furthermore, processors are now inherently parallel offering the execution of several diverse threads simultaneously. This paper proposes StreamSVM, the first algorithm for training linear Support Vector Machines (SVMs) which takes advantage of these properties by integrating caching with optimization. StreamSVM works by performing updates in the dual, thus obviating the need to rebalance frequently visited examples. Furthermore we trade off file I/O with data expansion on the fly by generating features on demand. This significantly increases throughput. Experiments show that StreamSVM outperforms other linear SVM solvers, including the award winning work of [38], by orders of magnitude and produces more accurate solutions within a shorter amount of time.

Categories and Subject Descriptors

I.5.2 [Pattern Recognition]: Design Methodology—*Classifier design and evaluation*

General Terms

Algorithm, Experimentation, Performance

Keywords

Support Vector Machines, Optimizatation, Coordinate Descent

1. INTRODUCTION

In this paper we propose StreamSVM an efficient method for large scale *single machine* linear Support Vector estimation, yielding the fastest currently available solver which runs on general purpose hardware by over an order of magnitude. To the best of our knowledge, this is the first work

^{*}The research was performed when visiting Purdue.

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on training linear Support Vector Machines (SVMs) which takes explicit advantage of different storage subsystems on a computer to achieve very high data throughput. In particular, our optimization algorithm is co-designed to take advantage of the fact that memory access is much faster than disk access. Our approach is entirely generic and can be extended to a more fine-grained storage hierarchy and a larger class of convex optimization problems.

1.1 Solvers for Training SVMs

Support Vector Machines (SVMs) have arguably revolutionized machine learning over the past decade. Fueled by their impressive performance in a number of real-world applications, there have been numerous efforts to scale SVMs to large data sets. Some notable contributions include Sequential Minimal Optimization (SMO) [28, 10], SVMLight [19], LaSVM [4], and gradient projection based solvers [39].

Much of the initial success of SVMs was attributed to the so-called *kernel trick* wherein training data is implicitly mapped to a high dimensional feature space, and a margin maximizing linear classifier is learned in this mapped space. In contrast, *linear* SVMs do not employ the kernel trick explicitly.¹ As massive, high-dimensional data sets are becoming commonplace, there is a recent surge of interest in linear SVMs. Some recent papers [20, 18, 17, 35] tackle this problem with great success, and provide algorithms with convergence guarantees. The above solvers either assume that the data resides in memory or that a cluster of machines with appropriate communication and synchronization facilities are available.

In an award winning paper [38] revisited the problem of training linear SVMs when the data does not fit into memory [29, 27, 19]. In a nutshell, the key idea is to split the data into manageable blocks, compress and store each block on disk, and perform dual coordinate descent by loading each block sequentially. This basic idea was improved upon by [11] who observed that the block minimization (BM) algorithm of [38] does not retain important points before discarding each block. They therefore, propose to retain some important points from the previous blocks in the RAM. This simple idea leads to significant speed-ups, and [11] demonstrate that their selective block minimization (SBM) algorithm outperforms BM. Here we propose a new algorithm StreamSVM, and show that it outperforms both BM and SBM on a number of publicly available datasets.

¹They may, however, use the kernel trick implicitly by hashing and direct feature expansion as described in [31].

1.2 Exploiting the Storage Hierarchy

StreamSVM takes advantage of the different characteristics inherent in the storage hierarchy of modern computers. That is, while hard disks excel at storing large amounts of data, they have typically mediocre data transfer rates and are outright slow at random access operations. Compared to that, main memory comes at a hundredfold premium in terms of space but offers two to three orders of magnitude faster data transfer rates. CPU caches are yet faster again. Similar considerations hold for solid state drives, PCI interconnects, and graphics subsystems.

This suggests that algorithms which require streaming through data from disk should take advantage of the data they already have in main memory while waiting for more data to arrive from disk. Obviously, the same rationale applies to a sequence of storage systems with different capacity/bandwidth characteristics. To make things somewhat more explicit we list a range of such systems below:

System	Capacity	Bandwidth	IOPs
Disk	3TB	150MB/s	10^2
SSD	256GB	500MB/s	$5 \cdot 10^4$
RAM	16GB	30GB/s	10^8
Cache	16MB	100GB/s	10^9

In the present paper we focus on two parts of this hierarchy — disk and memory (see Figure 1). This already affords quite dramatic improvements in terms of speed relative to sequential algorithms. In a nutshell our algorithm does the following:

Iterate over the data in main memory while streaming data from disk. Evict primarily examples from main memory that are “uninformative”.

A naive approach which takes, e.g. stochastic gradient descent steps based on the importance of examples would likely run afoul of data weighting problems — informative examples need not have extended statistical weight but rather only a higher influence on the choice of objective function. One way of dealing with this issue is to resort to dual updates. That is, we consider the dual optimization problem to SVMs (and related problems) and judiciously update the associated Lagrange multipliers. This leads to the following algorithm:

Reader

```

while not converged do
  read example  $(x, y)$  from disk
  if buffer full then evict random  $(x', y')$  from memory
  insert new  $(x, y)$  into ring buffer in memory
end while

```

Trainer

```

while not converged do
  randomly pick example  $(x, y)$  from memory
  update dual parameter  $\alpha$ 
  update weight vector  $w$ 
  if deemed to be uninformative then evict  $(x, y)$  from memory
end while

```

1.3 Enterprise Scale Solvers

To place our research in perspective note that industrial datasets regularly exceed the capacities offered by single computers in terms of both storage and computation. This

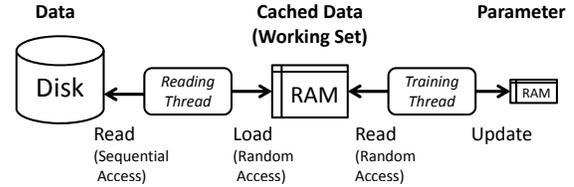


Figure 1: Basic data flow diagram of the optimization algorithm. A reader keeps on filling the main memory from disk while the optimization thread(s) perform updates on the data available in RAM. Both threads operate asynchronously.

means that distributed inference techniques are required. Unfortunately, large server centers often come with rather severe restrictions on reliability, inter-machine latency, communication trees, delays, etc. such that it is desirable to find algorithms which compute estimates using a bare minimum of communication. Note that not all estimation problems are amenable to efficient high-latency scenarios. For instance, latent variable models typically require excellent communication and great care needs to be taken to obtain fast estimates [2, 32]. A common attribute of these models is that they have a large degree of symmetry and non-convexity in their parametrization.

Fortunately much of what is commonly known as generalized linear models can be addressed with convex solvers. That is, the estimation problems can be decomposed efficiently into parts which are guaranteed to yield very similar solutions. In statistical learning terminology this is known as *stability* of the solution space and there exists a rich body of research [7, 36, 9, 16] extolling the desirable theoretical properties of convexly penalized estimation problems. It is therefore natural to take advantage of these properties in terms of implementations. For instance, [40] show that it is possible to perform stochastic gradient descent on individual processors *independently*, using random sub-samples of the data and to average the parameter estimates afterwards and *simultaneously* reaping the benefits of parallelization. Note that in previous work [25] suggested a similar averaging strategy, however their theoretical analysis only showed that averaging does not hurt, rather than actually accelerate convergence. It is in the spirit of [40] that we approach the problem of estimation:

1. Decompose (possibly with oversampling) the data for several machines.
2. Solve the estimation problem per machine as efficiently as possible.
3. Average the solutions between machines to obtain a final estimate.

Much work in the analysis of [40] was invested into proving that the stochastic gradient descent solutions on subsets are sufficiently independent for averaging to be actually beneficial. If we treat the optimization step as a standard batch problem this obstacle disappears. In this case we can appeal directly to the asymptotic analysis of [26] to see that averaging is beneficial: in particular, [26] show that the parameter distribution of a penalized empirical risk minimizer $w^*|X, Y$ conditioned on some data X, Y is asymptotically normal. This means that if we obtain such estimates based on vari-

ous subsets of data via $w^*|X_i, Y_i$, we will be able to aggregate this to an improved joint estimate via $\frac{1}{n} \sum_{i=1}^n w^*|X_i, Y_i$.

Consequently, in the present paper we are primarily concerned with step 2 of the above approach — to find the most efficient way of solving a convex optimization problem on a single machine. While we primarily focus on linear SVMs in this paper, our ideas are fairly generic and can be applied to other convex losses including losses used in structured prediction [3].

Outline. We will briefly review dual descent algorithms for linear Support Vector Machines in Section 2. Subsequently Section 3 gives a detailed description of the nested loop used in traversing through data in core memory and streaming from disk. Experimental results are provided in Section 4 and we conclude with a discussion in Section 5.

2. DUAL COORDINATE DESCENT

2.1 Support Vector Classification

In the following we assume that we are given n examples $x_i \in \mathcal{X}$ and labels $y_i \in \{\pm 1\}$ drawn independently and identically from some distribution $(x_i, y_i) \sim p(x, y)$. It is our goal to find some function $f: \mathcal{X} \rightarrow \mathbb{R}$ which minimizes the misclassification error, e.g. by minimizing the probability that $yf(x) \leq 0$. This constitutes the most basic of all estimation problems, namely that of binary classification. We simplify things further by assuming that $\mathcal{X} = \mathbb{R}^d$. This assumption will be relaxed subsequently when we discuss how to expand features on the fly. The primal formulation of a linear SVM can be written as follows² [13]:

$$\underset{w \in \mathbb{R}^d}{\text{minimize}} \frac{1}{2} \|w\|^2 + C \sum_{i=1}^n \max\{0, 1 - w^\top y_i x_i\} \quad (1)$$

Using standard convex optimization tools [8] the above problem can be rewritten in its dual form

$$\underset{\alpha}{\text{minimize}} D(\alpha) := \frac{1}{2} \alpha^\top Q \alpha - \alpha^\top \mathbf{1} \quad (2a)$$

$$\text{subject to } \mathbf{0} \leq \alpha \leq C \mathbf{1}. \quad (2b)$$

Here, Q is an $n \times n$ matrix whose entries are given by $Q_{ij} = y_i y_j x_i^\top x_j$, and $\mathbf{1}$ is the vector of all ones. The minimizer w^* of (1) and the minimizer α^* of (2) are related by the primal dual connection: $w^* = \sum_{i=1}^n \alpha_i^* y_i x_i$. The dual problem (2) is a Quadratic Programming (QP) with box constraints, and the i -th coordinate α_i corresponds to the i -th example (x_i, y_i) .

2.2 Dual Updates

The following coordinate descent scheme can be used to minimize the dual [18]:

- Initialize $\alpha^1 = \mathbf{0}$.
- At iteration t select coordinate i_t
- Update α^t to α^{t+1} via

$$\alpha_{i_t}^{t+1} = \underset{0 \leq \alpha_{i_t} \leq C}{\text{argmin}} D(\alpha^t + (\alpha_{i_t} - \alpha_{i_t}^t) \mathbf{e}_{i_t}) \quad (3a)$$

$$\alpha_i^{t+1} = \alpha_i^t \quad \text{if } i \neq i_t \quad (3b)$$

²We omit an explicit bias term since this greatly simplifies the dual problem while remaining entirely general since the bias can be introduced easily as an additional coordinate in the data.

Here, \mathbf{e}_i denotes the i -th standard basis vector. Since $D(\alpha)$ is a QP, the one-variable subproblem (3a) can be solved exactly (see [18] for details):

$$\alpha_{i_t}^{t+1} = \min \left\{ \max \left\{ 0, \alpha_{i_t}^t - \frac{\nabla_{i_t} D(\alpha^t)}{Q_{i_t i_t}} \right\}, C \right\}. \quad (4)$$

Here, $\nabla_{i_t} D(\alpha)$ denotes the i_t -th coordinate of the gradient. The above updates are also closely related to implicit updates of [21, 22, 12] and the passive aggressive updates of [14]. If we maintain $w^t := \sum_{i=1}^n \alpha_i^t y_i x_i$, then the gradient $\nabla_{i_t} D(\alpha)$ can be computed efficiently using

$$\nabla_{i_t} D(\alpha) = \mathbf{e}_{i_t}^\top (Q \alpha - \mathbf{1}) = w^{t \top} y_{i_t} x_{i_t} - 1. \quad (5)$$

w^{t+1} is kept related to α^{t+1} by computing

$$w^{t+1} = w^t + (\alpha_{i_t}^{t+1} - \alpha_{i_t}^t) y_{i_t} x_{i_t}. \quad (6)$$

A naive choice for i_t is to traverse examples periodically, which means that $i_t = t \bmod n$. We call an iteration from α^{nk} to $\alpha^{n(k+1)}$ an *outer iteration*, contrasted with an *inner iteration* from α^t to α^{t+1} . An alternative is to randomly permute the index and to access examples in arbitrary order. In that case, $i_t = \sigma^k(t \bmod n)$ where $\sigma^k \in \mathfrak{S}_n$ is a permutation.

3. PROPOSED METHOD

We assume that the entire dataset cannot fit in RAM, therefore there must exist an $\Omega < n$, which is an upper bound for the number of training data which can be stored in the RAM. Our algorithm, StreamSVM, maintains a set of indices A containing the indices of the data points in memory and ensures that $|A| \leq \Omega$ at all times. The major challenge is to select the working set carefully. Towards this end, we use a strategy that is inspired by shrinking [19, 18].

A remarkable property of the dual problem is that we can potentially reduce the size of optimization problem by carefully choosing the subset of the data. This property was first observed by [19] and is widely used in popular solvers such as SVMlight [19], LibSVM [10], and Liblinear [17]. We also aim to utilize the property and solve the entire problem efficiently. First we look at the following fact:

FACT 1. *Let α^* be the optimal solution of (2). Define A^{in} and A^{bo} as $\{i | \alpha_i^* = 0\}$ and $\{i | \alpha_i^* = C\}$ respectively. Any optimal solution of*

$$\underset{\alpha}{\text{minimize}} D(\alpha) \quad \text{subject to } \alpha_i = \begin{cases} 0 & \text{if } i \in A^{\text{in}} \\ C & \text{if } i \in A^{\text{bo}} \end{cases}$$

is also an optimal solution of (2).

A^{in} is called the *inactive set*, A^{bo} the *bound set*, and $A^{\text{ac}} := \{i | 0 < \alpha_i^* < C\}$ the *active set*. If w^* denotes the optimal primal solution corresponding to α^* , then using the KKT conditions [8] it follows that [29] $i \in A^{\text{in}}$ implies that $w^{* \top} y_i x_i > 1$. Similarly, $w^{* \top} y_i x_i < 1$ for all $i \in A^{\text{bo}}$. Intuitively, what this means is that data which is in the inactive set are well classified and the points in the bound set are not. If these sets are known beforehand, the solver can concentrate on data in the active set by setting the other coordinates to 0 or C accordingly and freezing them. Usually A^{ac} is a small fraction of the original dataset, and therefore it is

advantageous to focus most of the attention of the solver on identifying the coordinates in A^{ac} and optimizing them.

One way to identify the active set is to use projected gradients. For the bound constrained optimization problem (2) the i -th component of the projected gradient $\nabla_i^\pi D(\alpha)$ can be computed via

$$\nabla_i^\pi D(\alpha) := \begin{cases} 0 & \text{if } \alpha_i = 0 \text{ and } \nabla_i D(\alpha) > 0 \\ 0 & \text{if } \alpha_i = C \text{ and } \nabla_i D(\alpha) < 0 \\ \nabla_i D(\alpha) & \text{otherwise.} \end{cases} \quad (8)$$

The shrinking heuristic then works as follows: At the end of each outer iteration we compute $M_+ = \max_i \nabla_i^\pi D(\alpha)$ and $M_- = \min_i \nabla_i^\pi D(\alpha)$. During the inner iteration, if a point is found to satisfy $\alpha_i = 0$ and $\nabla_i D(\alpha) > M_+$ or $\alpha_i = C$ and $\nabla_i D(\alpha) < M_-$ then it is shrunk, that is, the corresponding α_i is frozen. If needed, the shrunk variables are visited at a later stage of the optimization process.

Maintaining the Working Set.

Just like in shrinking, points which are very well classified or very badly classified with respect to the current solution are eliminated. However, unlike shrinking our criterion is adaptive and also takes into account the size of the RAM. Let $\alpha^1, \dots, \alpha^n$ denote the intermediate solutions produced by the trainer in the last n steps. We compute

$$\varepsilon := \max_{t=1, \dots, n} |\nabla_{i_t}^\pi D(\alpha^t)|. \quad (9)$$

During the next n updates, a point is evicted from the RAM if $\alpha_i = 0$ and $\nabla_i D(\alpha) > \varepsilon$ or $\alpha_i = C$ and $\nabla_i D(\alpha) < -\varepsilon$. Furthermore, if the RAM is deemed to be full, that is, if $|A| > 0.9\Omega$ then we set $\varepsilon = \beta\varepsilon$ for some $\beta < 1$. We find that $\beta = 0.9$ works well in practice. In general, our criterion is more aggressive than shrinking, but this is not a problem; even if a point is evicted, it will subsequently be reloaded by the reader into the RAM, and the trainer will get a chance to update the corresponding α_i .

Synchronization Issues.

Detailed pseudo-codes of our scheme are shown in Algorithms 1 and 2. Our algorithm utilizes two threads namely the **reader** and **trainer**. The reader thread, as the name implies, is tasked with reading the data from disk and storing it into the RAM. If the RAM is full then the reader randomly replaces a cached point with the new data. The trainer thread randomly selects training points in the RAM and performs updates.

A major advantage of our method is that there is very little need for synchronization between the reader and the trainer. The only process which needs to be accessed mutually exclusively is the manipulation of the working set. In order to process the eviction and addition of data efficiently with the thread safety, we can utilize a widely used database management library. Then the only shared object we have to manage explicitly is the set of indices, A . This also means that our framework is fairly modular. For instance, the reader can read data from the disk, and perform pre-processing such as computing a feature vector $\phi(x)$ without affecting the trainer.

Stopping Criterion.

If the data resides in main memory then one can use the infinity norm of the projected gradient as an effective stop-

Algorithm 1 Reader

```

for  $k = 1, \dots, \text{max\_iter}$  do
  for  $i = 1, \dots, n$  do
    read  $y_i, x_i$  from the Disk
    calculate  $Q_{ii} = x_i^\top x_i$ 
    if  $|A| = \Omega$  then
      randomly select  $i' \in A$ 
      lock mutex
       $A = A \setminus \{i'\}$ 
      unlock mutex
      delete  $y_{i'}, Q_{i'i'}, x_{i'}$  from the RAM
    end if
    store  $y_i, Q_{ii}, x_i$  in the RAM
    lock mutex
     $A = A \cup \{i\}$ 
    unlock mutex
  end for
  if stopping criterion is met then
    exit
  end if
end for

```

ping criterion. However, in our case this is not possible. Therefore, we use the following approximate scheme: Suppose the trainer performs T updates in the time that it takes the reader to perform one scan through the data. With some abuse of notation let $\alpha^1, \dots, \alpha^T$ denote the intermediate solutions produced by the trainer. Then we compute

$$M_+ := \max_{t=1, \dots, T} \nabla_{i_t}^\pi D(\alpha^t), \quad M_- := \min_{t=1, \dots, T} \nabla_{i_t}^\pi D(\alpha^t), \quad (10)$$

and stop when the gap $M_+ - M_-$ falls below a pre-specified threshold, which is set to a default value of 0.0001. Note that since T is usually much larger than n the gap computed here is usually a more stringent stopping criterion than that used by either LibLinear, BM, or SBM. In our implementation, the trainer is responsible for computing the gap. When the gap is lower than the tolerance threshold or the number of iterations exceeds the maximum, then both the threads exit.

3.1 Proof of Convergence (sketch)

Our analysis builds upon the results of [24] and [18].

DEFINITION 1 (LUO AND TSENG PROBLEM). *Consider the following minimization problem*

$$\text{minimize}_\alpha \quad g(E\alpha) + b^\top \alpha \quad (11)$$

$$\text{subject to} \quad L_i \leq \alpha_i \leq U_i \quad (12)$$

where α and b are n -dimensional vectors, E is a $d \times n$ dimensional fixed matrix, $L_i \in [-\infty, \infty)$ and $U_i \in (-\infty, \infty]$ are upper and lower bounds respectively. The above optimization problem is a Luo and Tseng problem if the following conditions hold:

1. E has no zero columns
2. the set of optimal solutions \mathcal{A} is non-empty
3. the function g is strictly convex and twice continuously differentiable
4. for all optimal solutions $\alpha^* \in \mathcal{A}$, $\nabla^2 g(E\alpha^*)$ is positive definite.

The following result was implicitly shown in the proof of Theorem 1 in [18].

Algorithm 2 Trainer

```

 $\alpha^1 = \mathbf{0}, w^1 = \mathbf{0}, \varepsilon = 9, \varepsilon^{\text{new}} = 0, \beta = 0.9$ 
while stopping criterion is not met do
  for  $t = 1, \dots, n$  do
    while  $|A| = 0$  do
      sleep
    end while
    if  $|A| > 0.9 \times \Omega$  then
       $\varepsilon = \beta\varepsilon$  // aggressively shrink  $\varepsilon$ 
    end if
    randomly select  $i \in A$ 
    read  $y_i, Q_{ii}, x_i$  from the RAM
    compute  $\nabla_i D := w^{t\top} y_i x_i - 1$ 
    if  $(\alpha_i^t = 0 \text{ and } \nabla_i D > \varepsilon)$  or  $(\alpha_i^t = C \text{ and } \nabla_i D < -\varepsilon)$ 
    then
      lock mutex
       $A = A \setminus \{i\}$ 
      unlock mutex
      delete  $y_i, Q_{ii}, x_i$  from the RAM
      continue
    end if
     $\alpha_i^{t+1} = \min\{\max\{0, \alpha_i^t - \frac{\nabla_i D}{Q_{ii}}\}, C\}$ 
     $w^{t+1} = w^t + (\alpha_i^{t+1} - \alpha_i^t) y_i x_i$ 
    if  $\varepsilon^{\text{new}} < |\nabla_i D|$  then
       $\varepsilon^{\text{new}} = |\nabla_i D|$ 
    end if
  end for
  Update stopping criterion
   $\varepsilon = \varepsilon^{\text{new}}$ 
end while

```

LEMMA 1. *If no data $x_i = \mathbf{0}$, then the SVM dual (2) is a Luo and Tseng problem.*

PROOF. Set $E_{ij} = y_i x_j^\top \mathbf{e}_j, b = \mathbf{1}, L_i = 0, U_i = C$ and $g(\cdot) = \frac{1}{2} \|\cdot\|^2$. E has no zero columns because of our assumption that $x_i \neq \mathbf{0}$. Since the constraint set is bounded, and g is a strictly convex function, the set of optimal solutions \mathcal{A} is non-empty. Since g is twice differentiable and strongly convex, $\nabla^2 g$ is positive definite everywhere. \square

DEFINITION 2 (ALMOST CYCLIC RULE). *There exists an integer $B \geq n$ such that every coordinate is iterated upon at least once every B successive iterations.*

THEOREM 2 (THEOREM 2.1 OF [24]). *Let $\{\alpha^t\}$ be a sequence of iterates generated by a coordinate descent method (3) using the almost cyclic rule. The α^t converges linearly to an element of \mathcal{A} .*

All that remains to establish the convergence our coordinate descent scheme is to show it satisfies the almost cyclic rule. If the trainer accesses the cached training data sequentially, and the following conditions hold:

- The trainer is at most $\kappa \geq 1$ times faster than the reading thread, that is, the trainer performs at most κ coordinate updates in the time that it takes the reader to read one training data from disk.
- A point is never evicted from the RAM unless the α_i corresponding to that point has not been updated.

Then it is easy to see that this corresponds to the almost cyclic rule with $B = \kappa n$. In practice, the trainer randomly

accesses training points from the RAM, and if the RAM is full then the reader evicts a random point. Therefore, there is a very small probability that a point is evicted before the corresponding α_i is updated. However, in practice we find that this does not affect convergence.

4. EXPERIMENTS

We performed experiments on some of the largest publicly available data sets for binary classification to evaluate the performance of our algorithm, and to compare it with two existing methods namely SBM and BM. Since SBM was shown to be competitive with other methods such as Vowpal Wabbit (VW), Stochastic Gradient Descent (SGD), or Pegasos we do not explicitly compare against them here.

Datasets.

Table 1 summarizes the datasets used in our experiments. The webspam trigram dataset `webspam-t`³, as well as the KDD cup 2010 dataset `kddb`⁴ are from the LibSVM binary data collection⁵. The `dna` and `ocr` datasets were obtained from the Pascal Large Scale Learning Workshop website [34]. For all the datasets we randomly selected 80% of the labeled data for training and the remaining 20% for testing. Besides being massive, note that our datasets cover a variety of real-life scenarios. The `dna` dataset is dense, but is heavily imbalanced in terms of the number of positive vs negative examples. The `ocr` dataset is dense and balanced while both `kddb` and `webspam-t` are very sparse but very high dimensional.

Implementation Details.

Our code is implemented in portable C++ and uses POSIX threads (pthreads) for multi-threaded programming. In order to cache the data efficiently we use a thread safe in-memory hash table (StashDB) provided by Kyoto Cabinet⁶. We empirically find that the use of Kyoto Cabinet reduces our memory footprint by approximately 25% compared to SBM and BM. Open-source code as well as all the scripts needed to reproduce our experimental results will be made available for download from <http://www.stat.purdue.edu/~vishy>.

Hardware.

All experiments were conducted on the `Rossmann` computing cluster at Purdue University⁷, where each node has two 2.1 GHz 12-core AMD 6172 processors with 48 GB physical memory per node.

Experimental Setup.

We focused our study on the following two aspects: How does StreamSVM compare with SBM and BM for different values of C ? How does the size of RAM Ω affect the performance of StreamSVM? We also conducted an experiment

³Originally from <http://www.cc.gatech.edu/projects/doi/WebbSpamCorpus.html>.

⁴This dataset was derived from KDD CUP 2010 second problem `bridge_to_algebra_2008_2009`.

⁵<http://www.csie.ntu.edu.tw/~cjlin/libsvmtools/datasets/binary.html>.

⁶<http://fallabs.com/kyotocabinet/>

⁷<http://www.rcac.purdue.edu/userinfo/resources/rossmann>.

Table 1: Summary of the datasets used in our experiments. n is the total # of examples, d is the # of features, s is the feature density (% of features that are non-zero), $n_+ : n_-$ is the ratio of the number of positive vs negative examples, Datasize is the size of the data file on disk, Ω is the number of points in the RAM, and each Blocks is the data split used for the corresponding method. M denotes a million.

dataset	n	d	$s(\%)$	$n_+ : n_-$	Datasize	Ω	SBM Blocks	BM Blocks
ocr	3.5 M	1156	100	0.96	45.28 GB	150,000	40	20
dna	50 M	800	25	3e-3	63.04 GB	700,000	60	30
webspam-t	0.35 M	16.61 M	0.022	1.54	20.03 GB	15,000	20	10
kddb	20.01 M	29.89 M	1e-4	6.18	4.75 GB	2,000,000	6	3

where we expanded the features on the fly to demonstrate the scalability and flexibility of our framework. Since the objective functions we are minimizing are strongly convex, all optimizers will converge the same solution (within numerical precision) and produce the same generalization performance eventually. Therefore, what we are specifically interested in is the rate at which the objective function decreases.

4.1 Results

Varying C .

As C increases the effect of the regularizer decreases and the non-smooth hinge loss dominates the primal objective. Intuitively, this means that the problem becomes harder to solve for large values of C . Therefore, it is important to test an optimization algorithm across a range of C values.

We used $C \in \{0.0001, 0.001, \dots, 1000.0\}$ to test the performance of StreamSVM and contrast it with SBM and BM. For this experiment we set the maximum number of iterations for all algorithms to be 100; for StreamSVM an iteration is defined as one complete pass through the data by the reader thread. The stopping tolerance was set to be 10^{-3} . A job is killed by the queue manager on the cluster if it does not finish within 48hrs. Besides the space required to store the weight vector w and the coefficients α , both algorithms were allowed to use up to 2GB of extra RAM.

In Figure 2 we used the `dna` dataset and plot the relative function value difference as function of wall clock time for SBM, BM, and StreamSVM⁸. The relative function value difference is defined as $(D^* - D^t)/D^*$ where D^t is the dual objective function output by the optimizer at the t -th iteration, and D^* is the largest dual objective function value produced by either SBM, BM, or StreamSVM for the same parameter settings. Note that since we are plotting the y-axis on a log scale some points with relative function value difference of 0 are not displayed.

In the top three plots in Figure 3 we used the same value of $C = 1.0$, and plot the convergence behavior of SBM, BM, and StreamSVM on `kddb`, `ocr`, and `webspam-t` datasets respectively.

Note that in almost all cases, StreamSVM outperforms SBM and BM comprehensively. In particular, on the `dna`, `ocr` and `webspam-t` datasets, StreamSVM has converged to a high accuracy solution even before BM and SBM have managed to read and compress the data during the first pass. Also, both SBM and BM do not converge for large values of C on the `dna` dataset.

⁸Due to lack of space we plot only a subset of our results here. Detailed results including the generalization error and evolution of the gap can be found at <http://www.r.dl.itc.u-tokyo.ac.jp/~masin/Appendix.pdf>.

On the `kddb` dataset, StreamSVM does not exhibit any particular advantage over SBM. We investigated this further and found that the major bottleneck here is the need to frequently access random elements of w (29.89 million entries) and α (20.01 million entries), both of which are stored as dense vectors. This causes a number of cache misses and consequently slows down the trainer. On the other hand, since the data is very sparse (maximum 200 nonzero entries per training example) the reader thread is very fast. Consequently StreamSVM is not faster than SBM, although it is very competitive as compared to BM.

Varying Ω .

Next we study the effect of varying the RAM size Ω on the performance of StreamSVM. The same setup as the previous experiment was used here but with two notable changes. First, we fixed the value of C to be 1.0 for all datasets. Second, we set the RAM size to 256MB, 1GB, 4GB, and 16GB respectively.

On all datasets, the performance with 256MB RAM was inferior compared to higher RAM sizes. However, increasing the RAM size beyond 4GB does not help much. In fact, on the `kddb` dataset, increasing the size of RAM actually reduces performance. The reasons are as the same as above. Even though we can cache a large number of training points, the bottleneck here is in updating w and α . Therefore, the increase in Ω does not significantly improve convergence speed. For the `ocr` and `webspam-t` datasets there is marginal improvement when moving from 4GB to 16GB. This could potentially be explained by the fact that at the final solution there were relatively few training examples in the active set (675 for `ocr` and 1509 for `webspam-t`). We conjecture that large RAM sizes help only if the number of non-zero entries per training example are large, or of the dataset is very high dimensional and noisy because of which a large number of training examples are in the active set.

Expanding Features on the Fly.

In our final experiment, our aim is to show that explicit feature expansion can be easily incorporated into our framework. We computed an expanded feature $\phi(x)$ before the reader stores an example in RAM. Following the work of [33], we used `dna` dataset at http://sonnenburgs.de/soeren/projects/coffin/splice_data.tar.xz and computed the same feature corresponding to the feature referred to as “WD $d = 8$ explicit”. This results in a sparse feature vector of 12,495,240 dimensions. We examined the performances with RAM size of 16GB and 32GB.

We plot the relative function value difference vs wall clock time in the bottom plot of Figure 3. The same figure also shows how the gap is decreasing as a function of wall clock

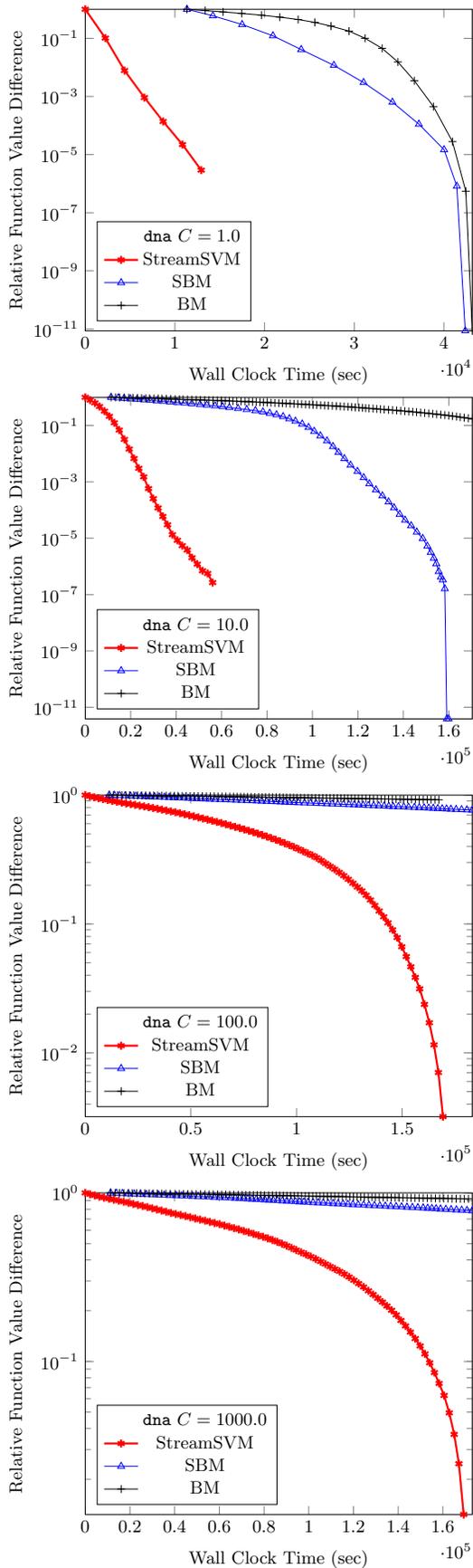


Figure 2: Relative function value difference vs wall clock time on the dna dataset for various values of C

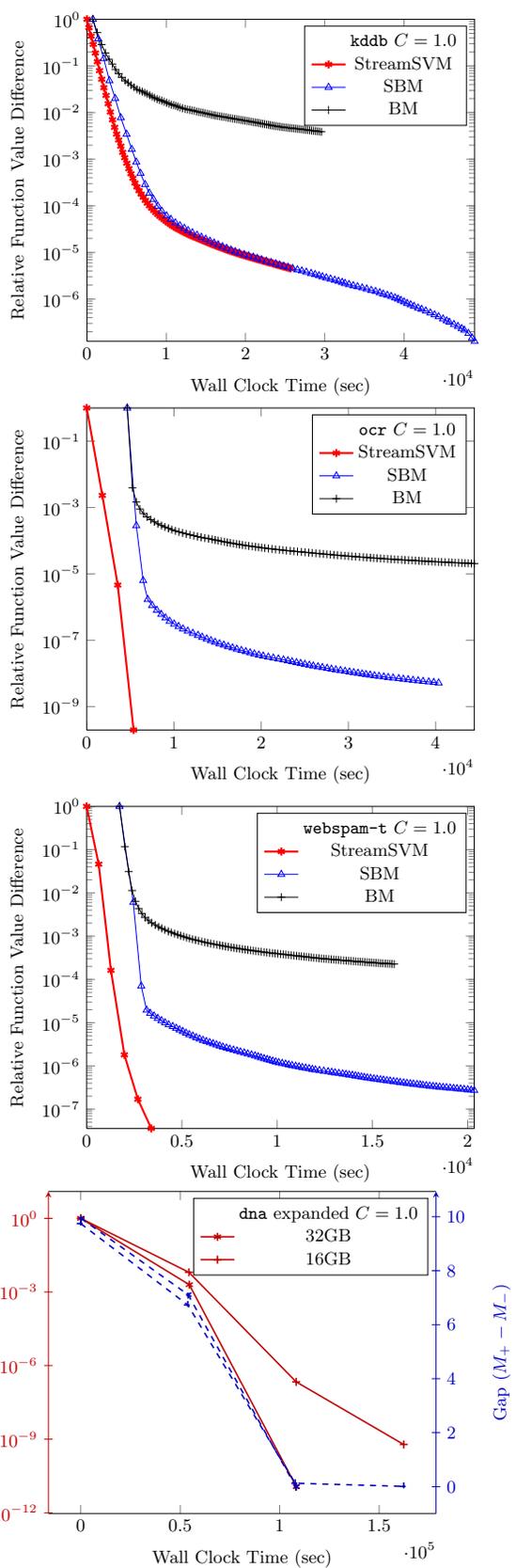


Figure 3: Top three figures: Relative function value vs wall clock time on the kddb, ocr, and webspam-t datasets for $C = 1.0$. The bottom figure: The red solid curve is the relative function value vs wall clock time and the blue dashed curve is the gap vs wall clock time on the dna dataset expanded using the “WD $d = 8$ explicit” feature with $C = 1.0$.

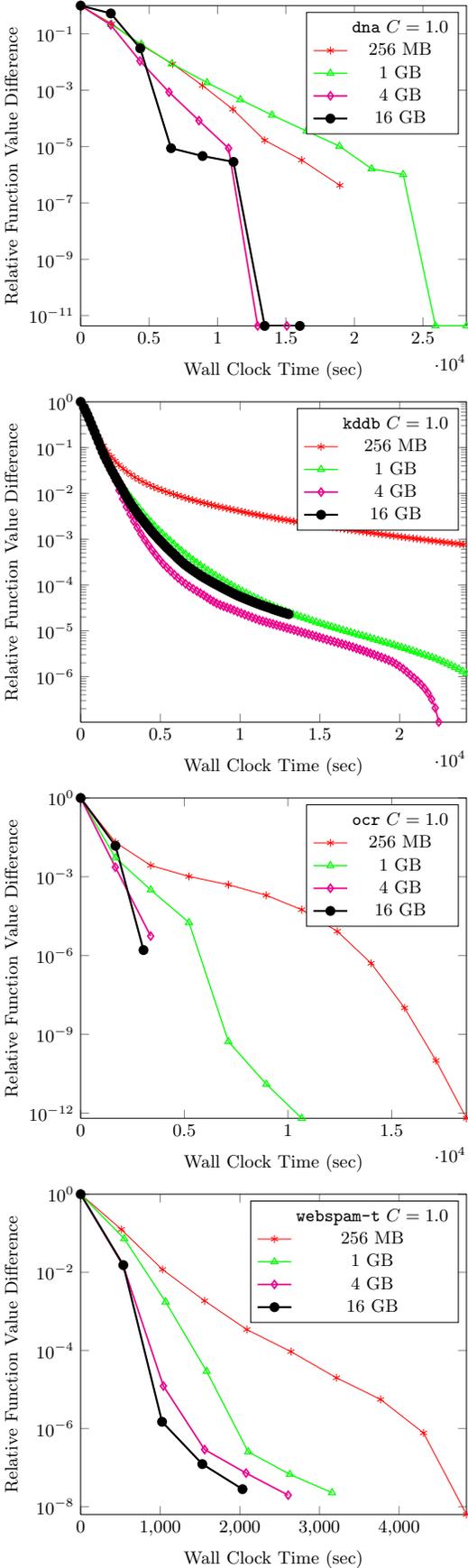


Figure 4: Relative objective function value as a function of wall clock time for various datasets as the RAM size Ω is varied.

time. In less than 10 hours StreamSVM could finish the optimization in both cases. To put our results in perspective, contrast this with the recent work of [1] who use a very similar feature representation as ours and train using LBFGS. Their algorithm requires around 30 iterations with each iteration requiring approximately 1 hour when distributed across 100 machines (total of 3000 CPU hours).

5. DISCUSSION

To conclude we would like to discuss some related work and place our contributions in perspective. Perhaps the closest in spirit to our work is the online version of SBM described by [11]. Here, data is assumed to arrive in a streaming fashion, and a dual coordinate descent procedure is used on blocks of data. However, there are some important differences between the two methods. First, online SBM only looks at the data once, while our algorithm StreamSVM performs multiple passes through the data. Second, the reading and the training in SBM happen in a synchronous fashion, while our reader thread asynchronously reads and caches data. In our experiments, StreamSVM also achieves near-optimal generalization performance after just one or two passes through the data.

In order to speed up linear SVMs where the feature mapping can be computed explicitly, [33] introduced a computational framework for linear SVMs (COFFIN). Along the same lines, one can use hash functions to map sparse high-dimensional features into dense low-dimensional features. This idea, first described in [31] and improved upon in [37] is one of the important reasons why the Vowpal Wabbit learning framework is fast. These techniques are very naturally incorporated into our framework as demonstrated in our experiments.

A closely related alternative to dual coordinate descent is stochastic gradient descent (SGD). Recently there have been numerous variants of which have been studied both theoretically [6, 30] as well as empirically [5]. However, SGD in its most basic form is inherently serial, and therefore there is a recent flurry of activity on developing parallel stochastic gradient descent solvers [23, 40, 15]. As explained in Section 1.3 we view our research as complementary.

Our long term research goal is to design, analyze, and implement novel optimization algorithms that take advantage of modern hardware to enable learning on and mining of massive datasets. With the increasing availability of many-core processors, general-purpose graphics processing units, and solid-state drives, we are witnessing a hardware revolution. The next generation of systems-aware, efficient, scalable machine learning algorithms need to take advantage of these emerging computing paradigms. We view this paper as a step towards that direction.

6. REFERENCES

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